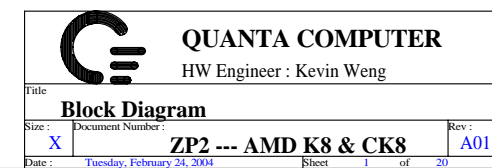
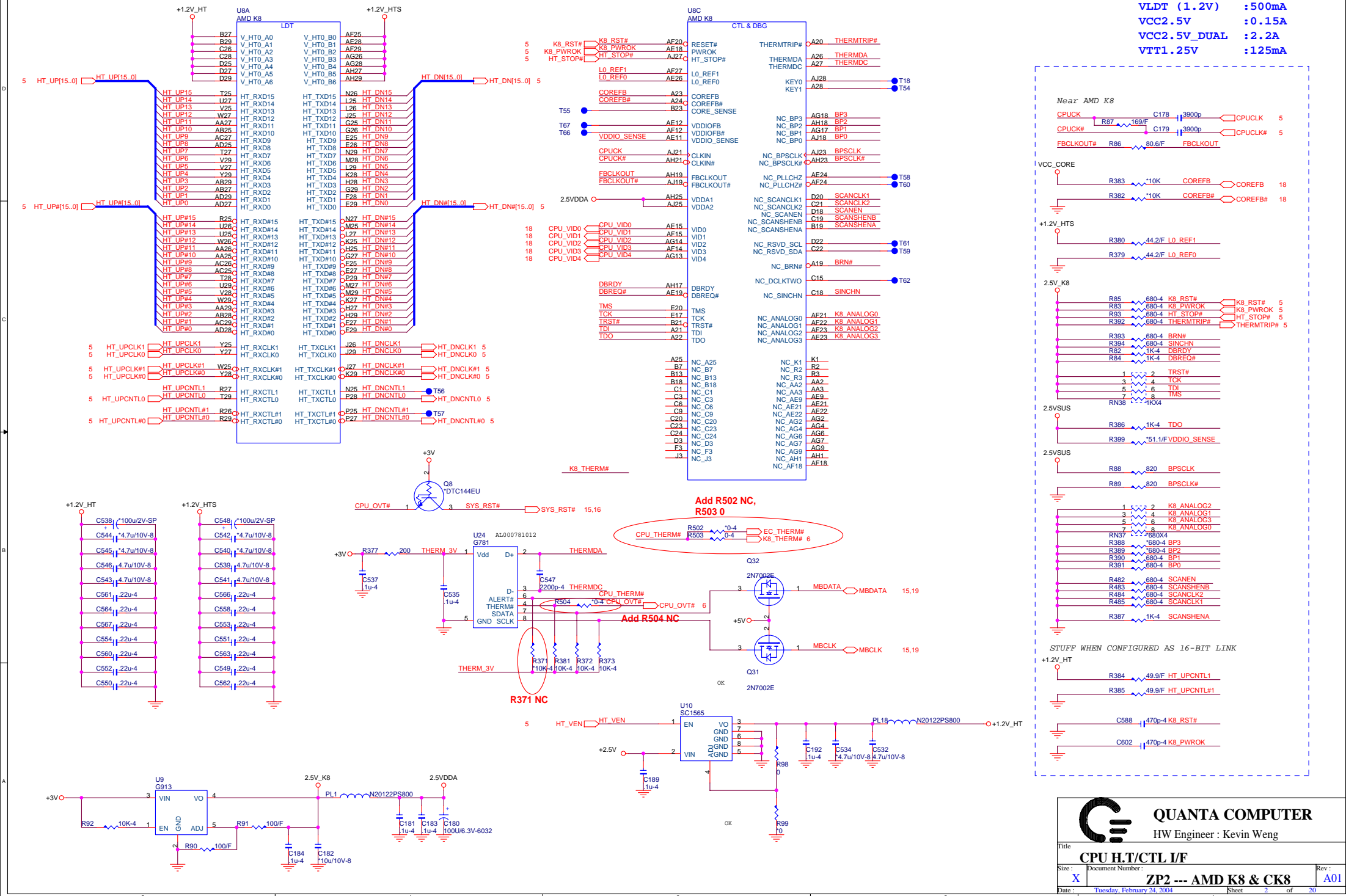


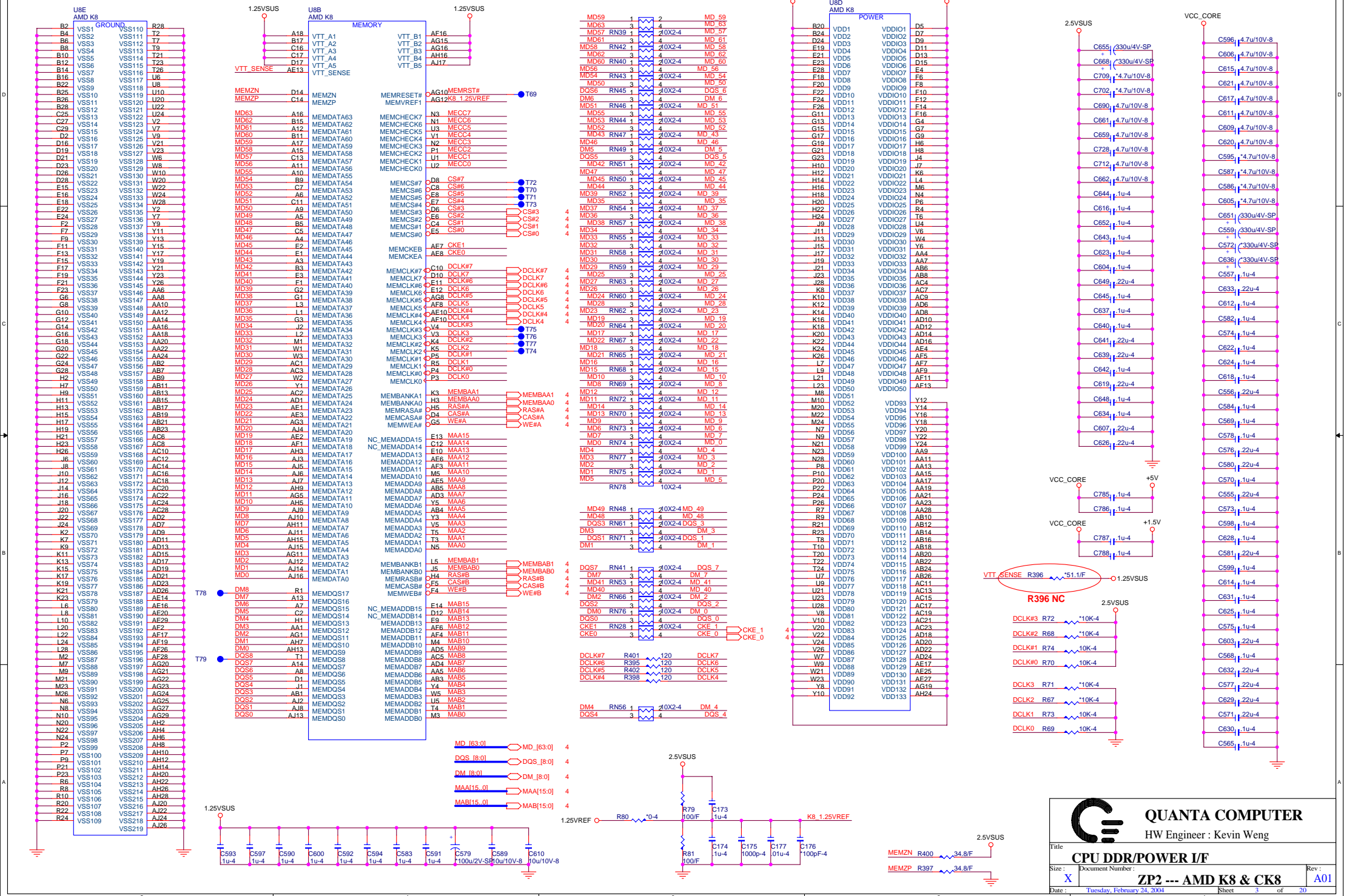
**2004/1/28**



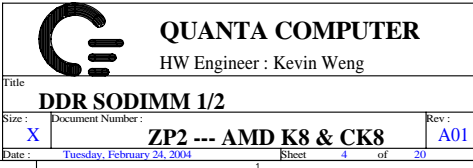
## CPU H.T/CTL I/F



## CPU DDR/POWER I/F

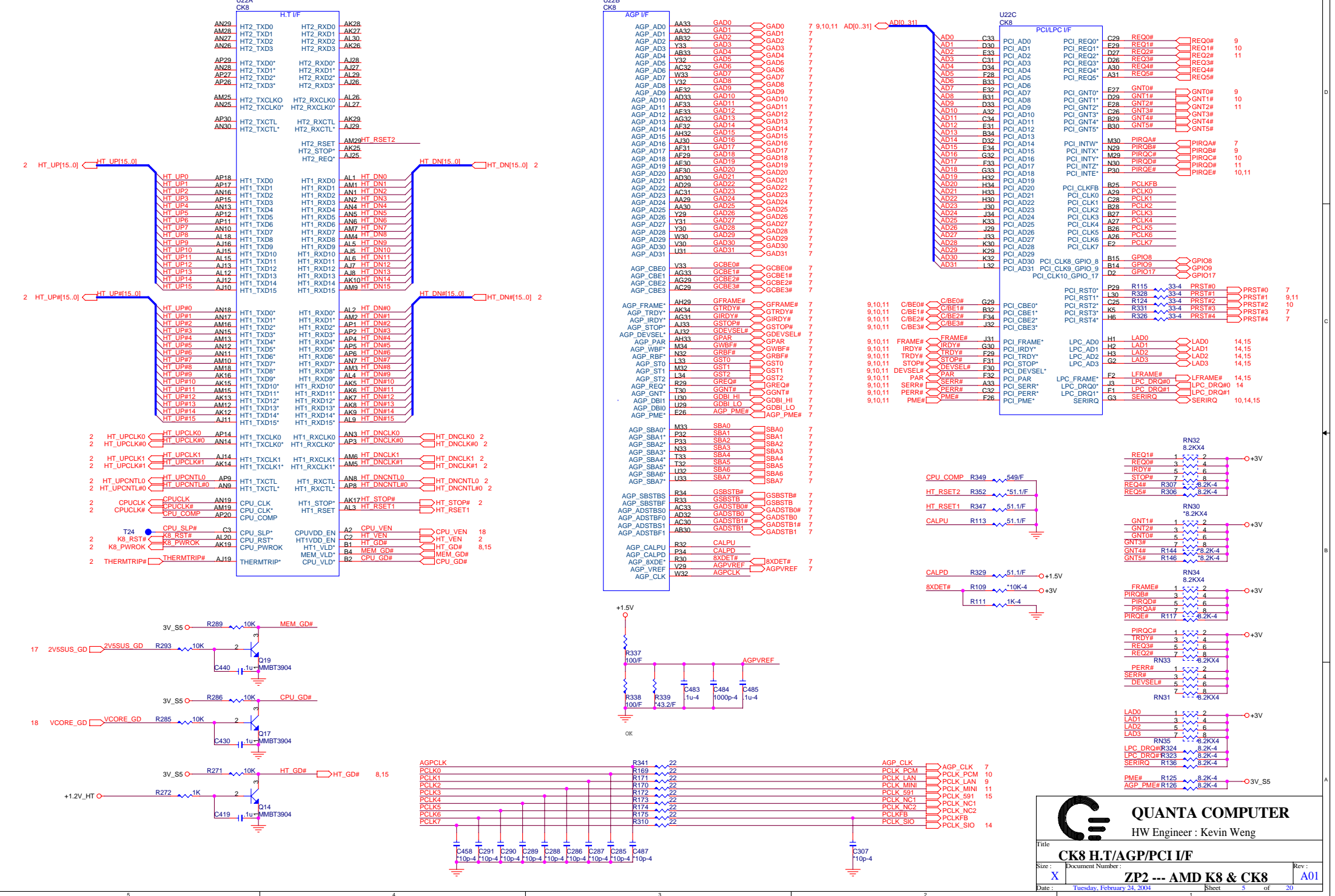



**RN80, 83, 86, 89, 100, 104, 108, 111 change value 68 -> 47**





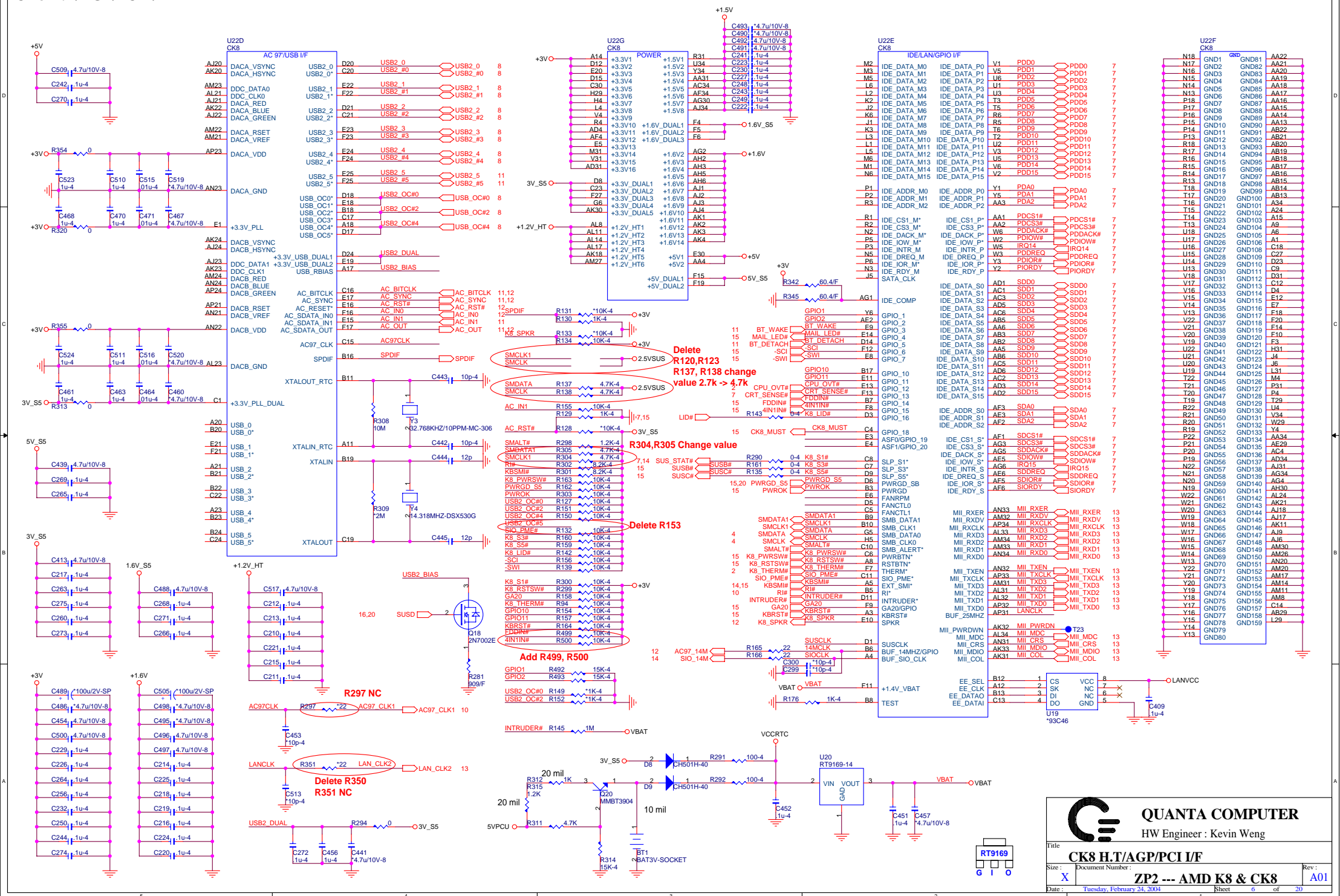
CK8 H.T/AGP/PCI I/F



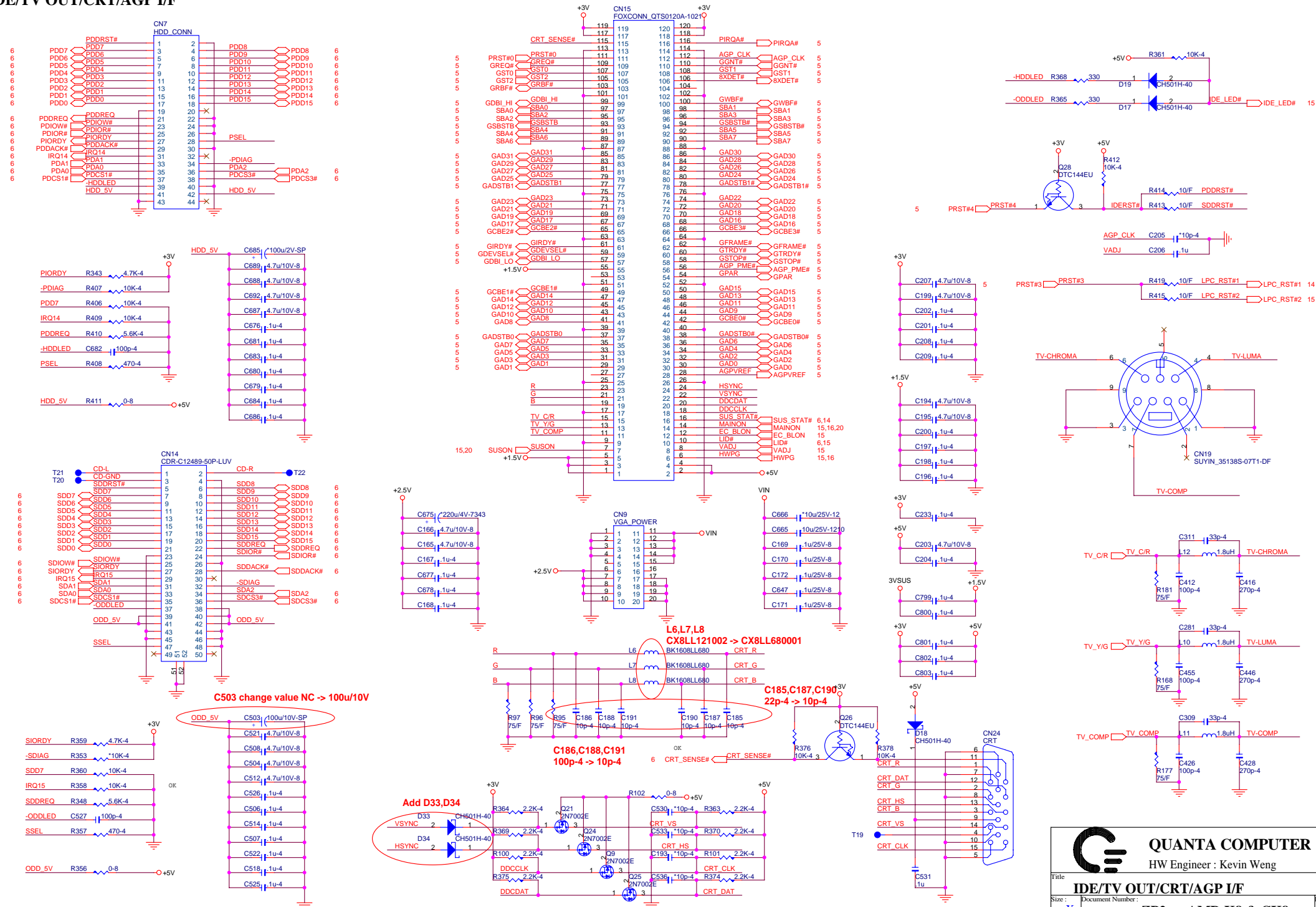


**QUANTA COMPUTER**  
HW Engineer : Kevin Weng

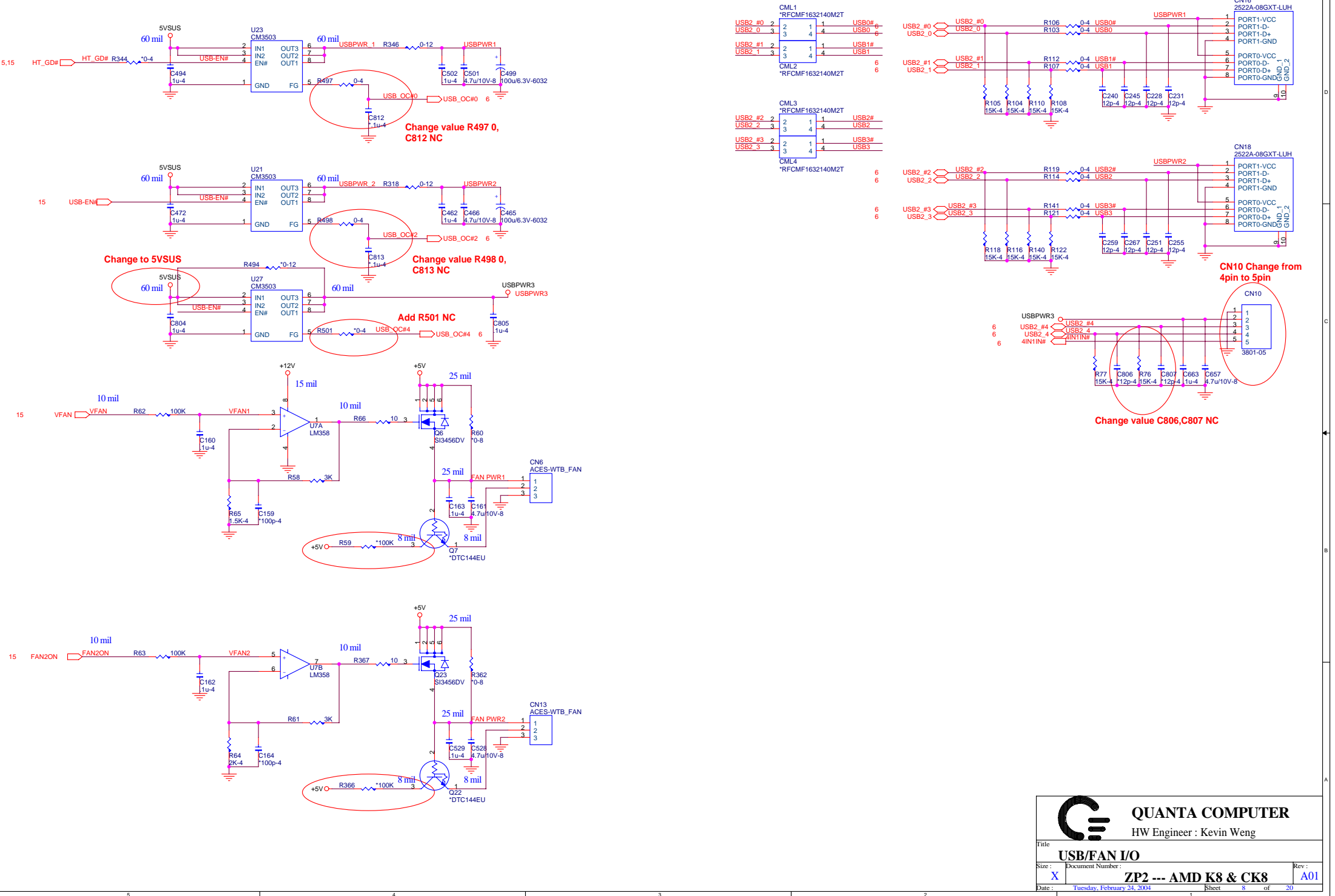
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Size: <b>X</b>	Document Number: <b>XP2 --- AMD K8 &amp; CK8</b>	Rev: <b>A01</b>
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**CK8 H.T/AGP/PCI I/F**

**IDE/TV OUT/CRT/AGP I/F**



USB/FAN I/O





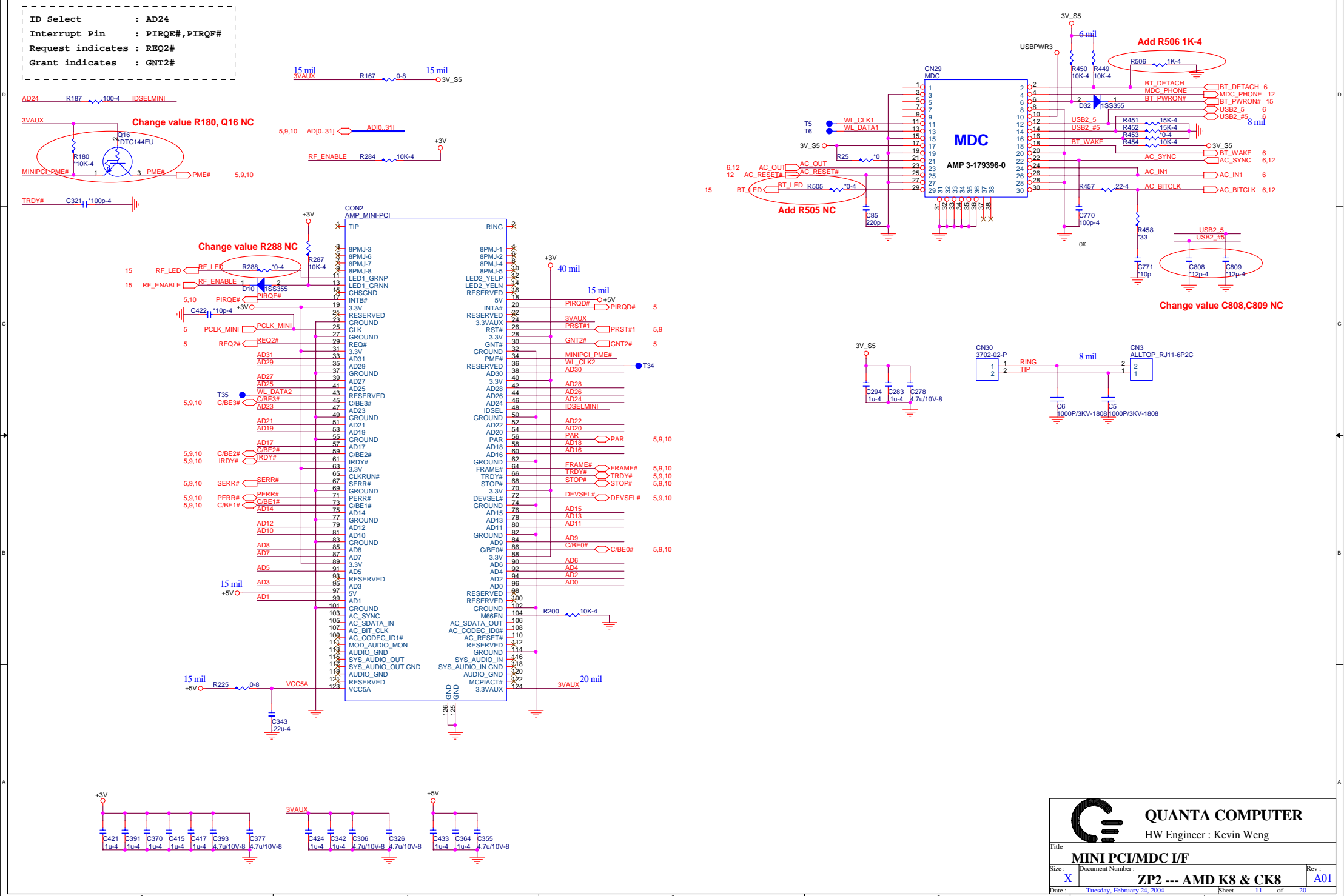




```

ID Select      : AD24
Interrupt Pin   : PIRQE#,PIRQF#
Request indicates : REQ2#
Grant indicates  : GNT2#

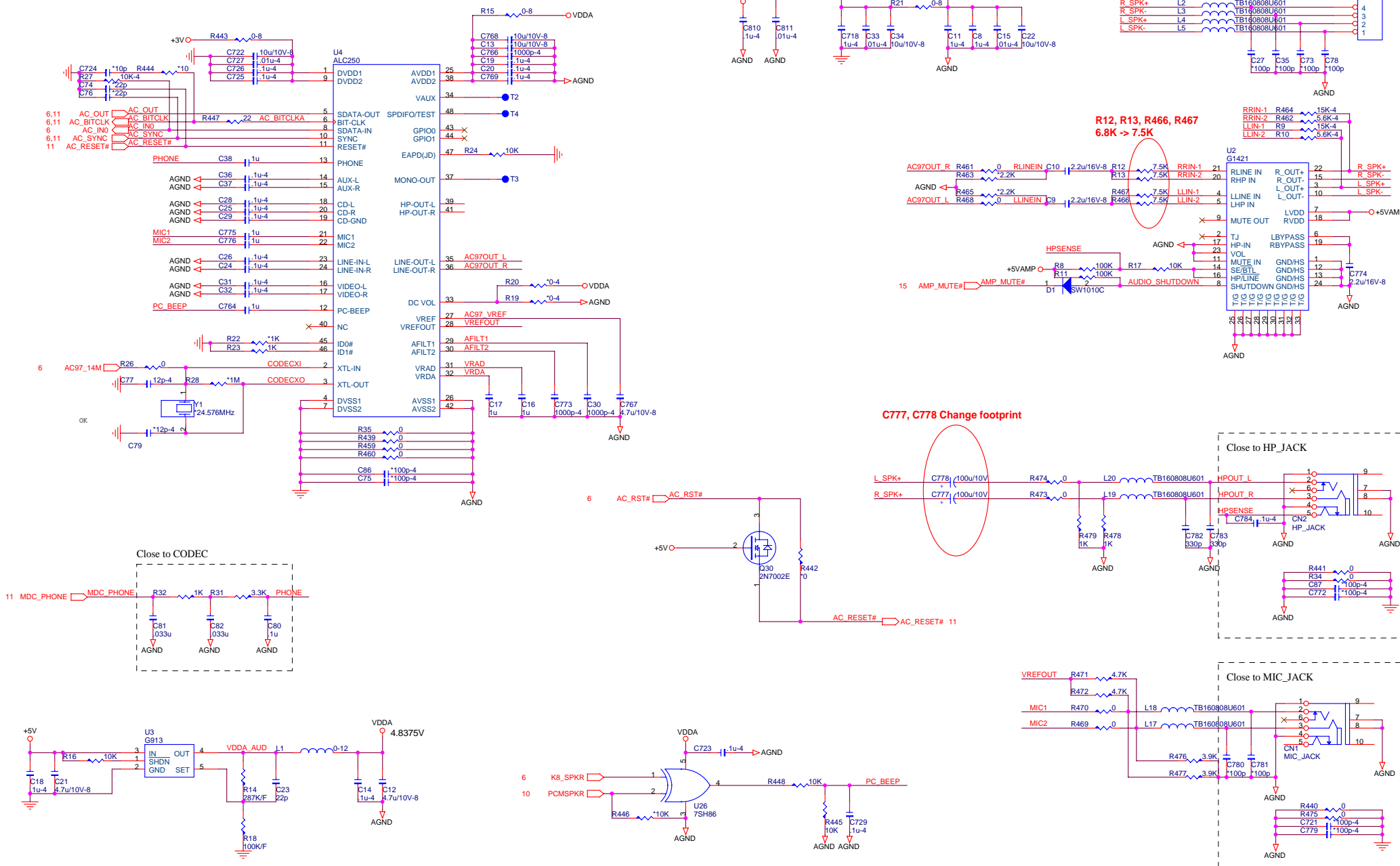
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HW Engineer : Kevin Weng

Title			
<b>MINI PCI/MDC I/F</b>			
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AC' 97 CODEC





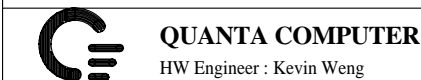
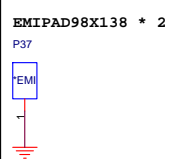
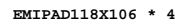
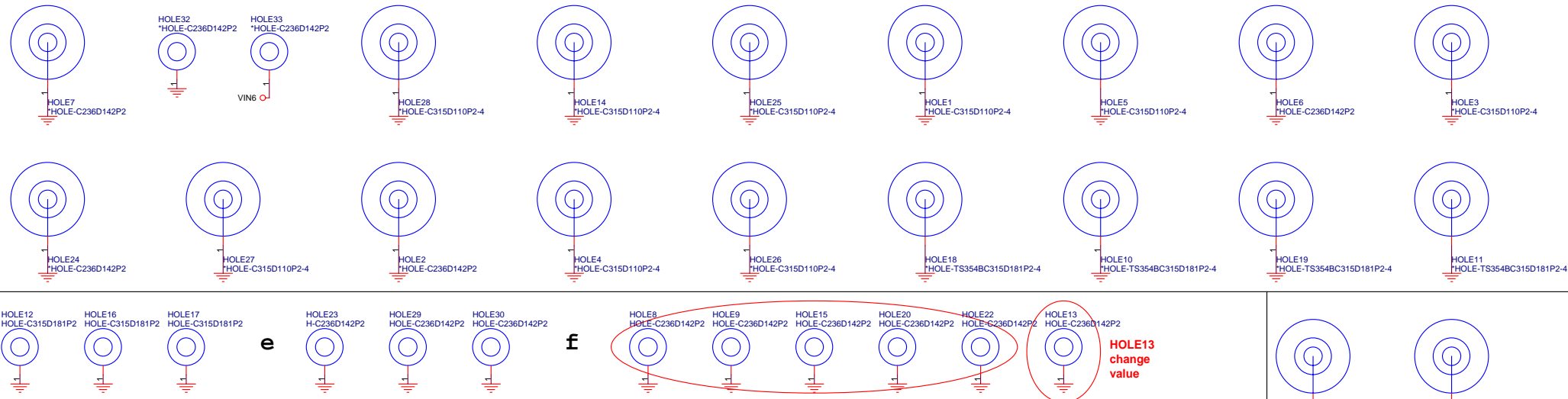
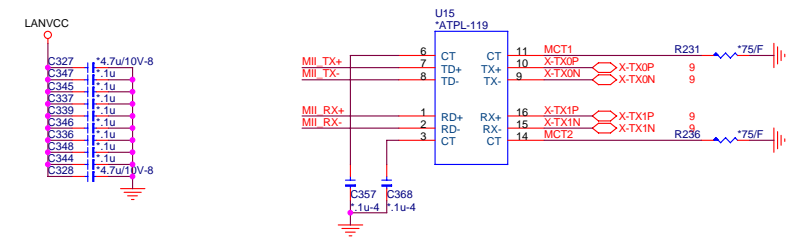
**QUANTA COMPUTER**

HW Engineer : Kevin Weng

Title <b>AC' 97 CODEC</b>		
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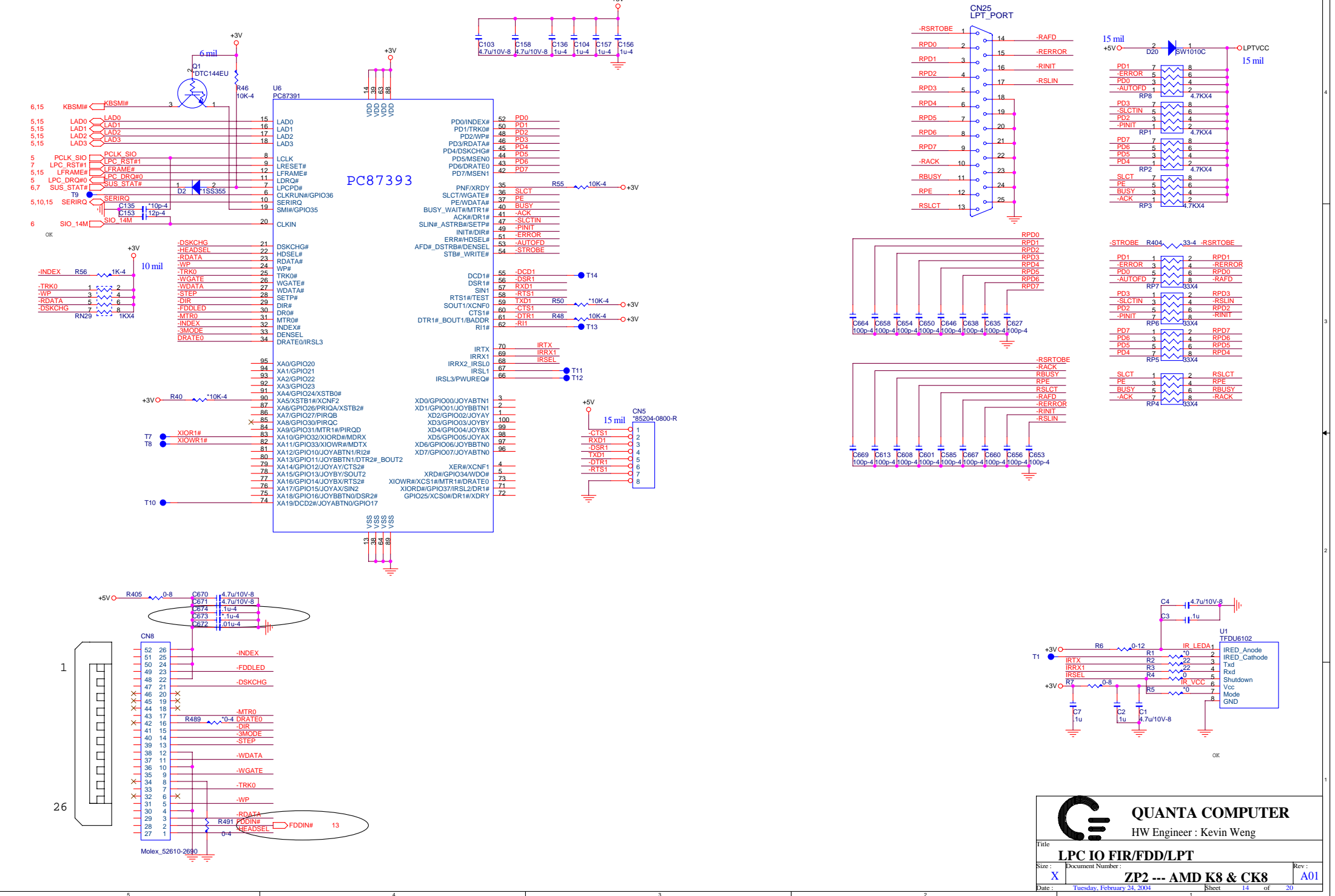



## 10/100 LAN PHY



Title <b>10/100 LAN PHY</b>			
Size : <b>X</b>	Document Number : <b>2P2 --- AMD K8 &amp; CK8</b>	Rev : <b>A01</b>	
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LPC IO FIR/FDD/LPT





**QUANTA COMPUTER**  
HW Engineer : Kevin Weng

Title

**LPC IO FIR/FDD/LPT**

Size : X

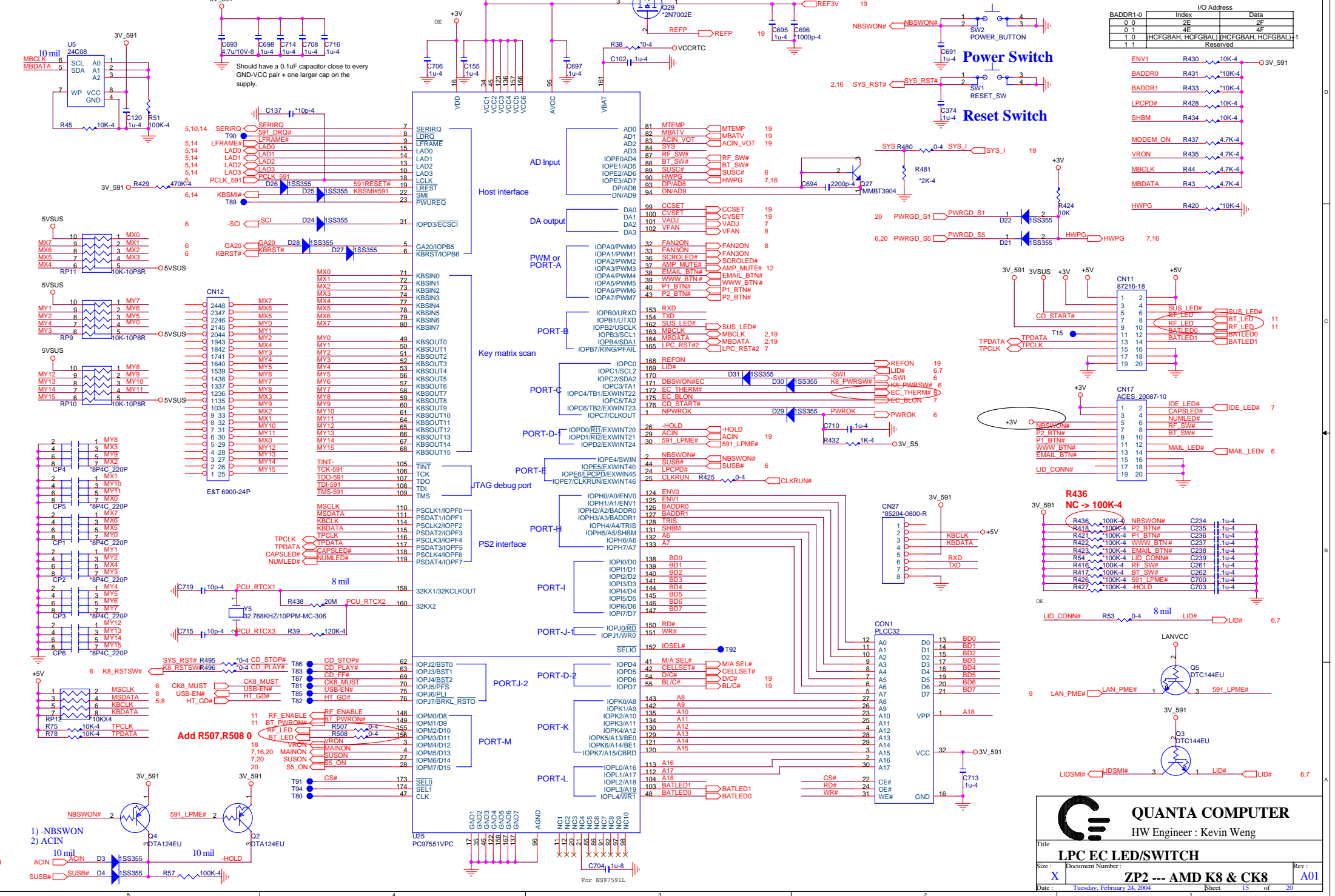
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LPC EC LED/SWITCH





**QUANTA COMPUTER**  
HW Engineer : Kevin Weng

Title

**LPC EC LED/SWITCH**

Size: X

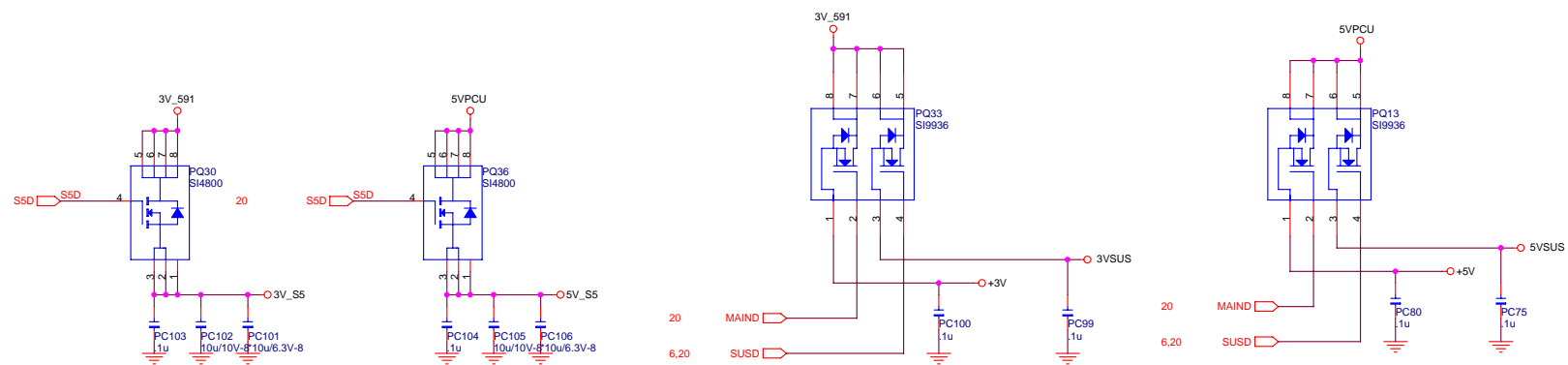
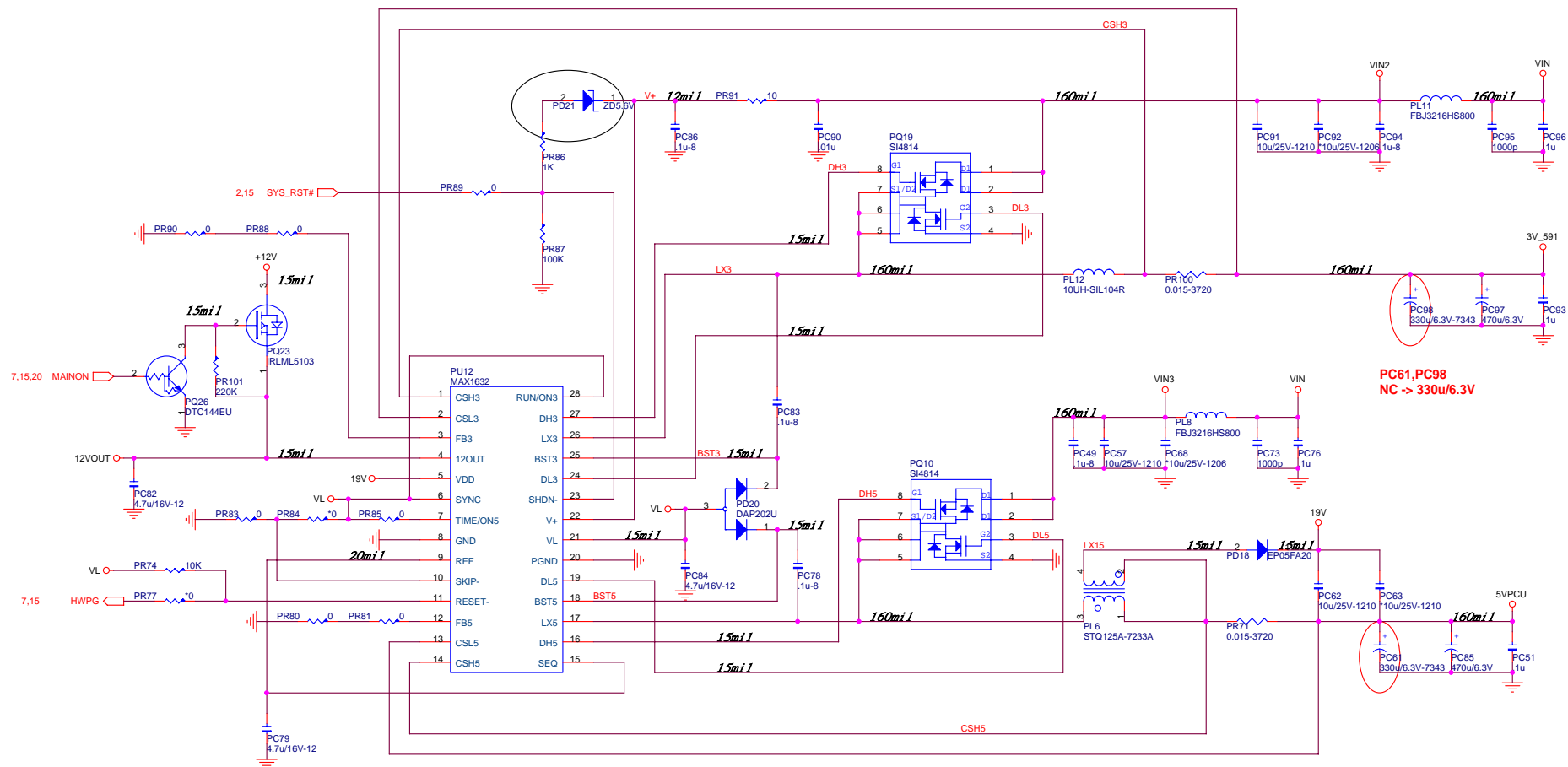
Document Number: **XP2 --- AMD K8 & CK8**

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## System Power



**QUANTA COMPUTER**

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## System Power

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## ZP2 --- AMD K8 & CK8

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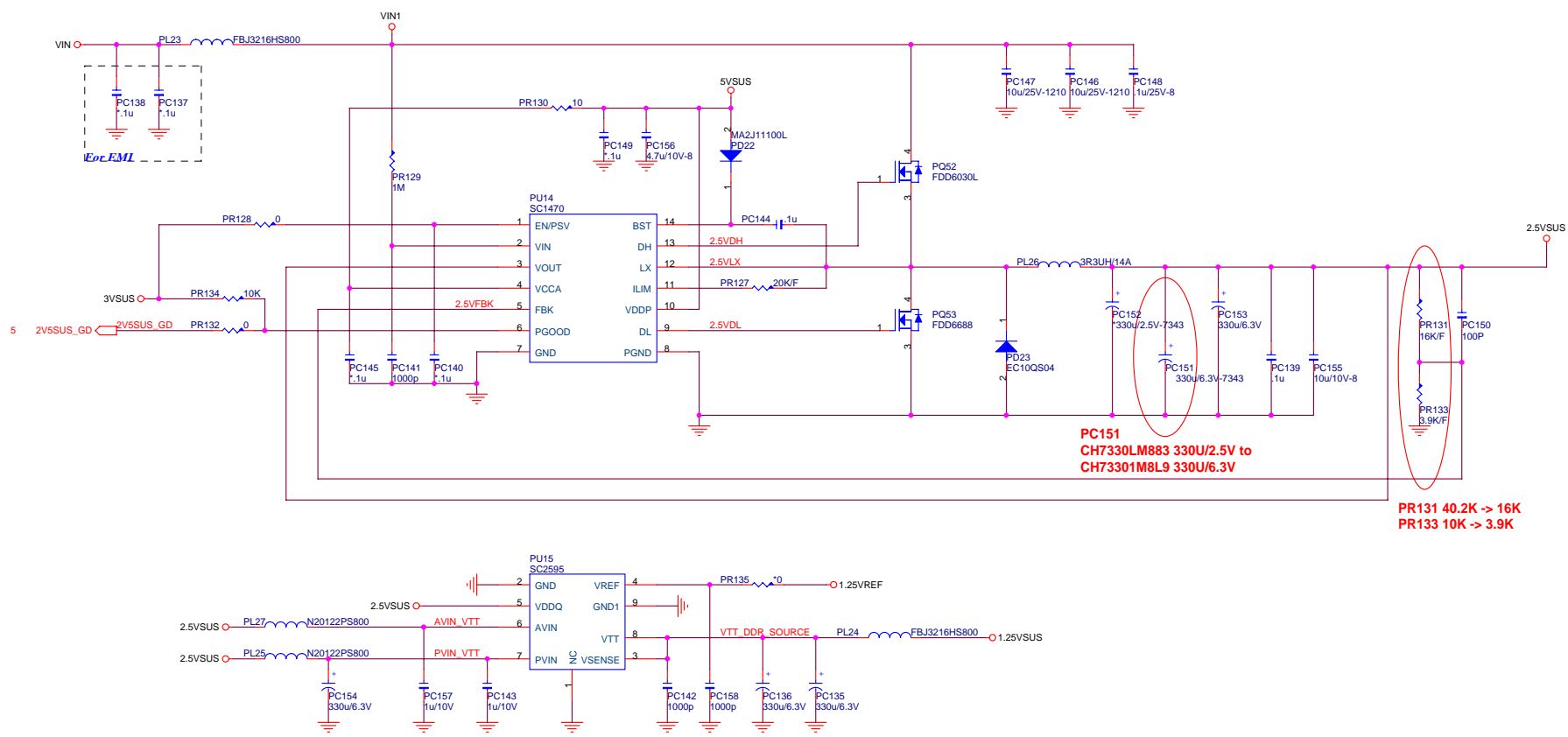
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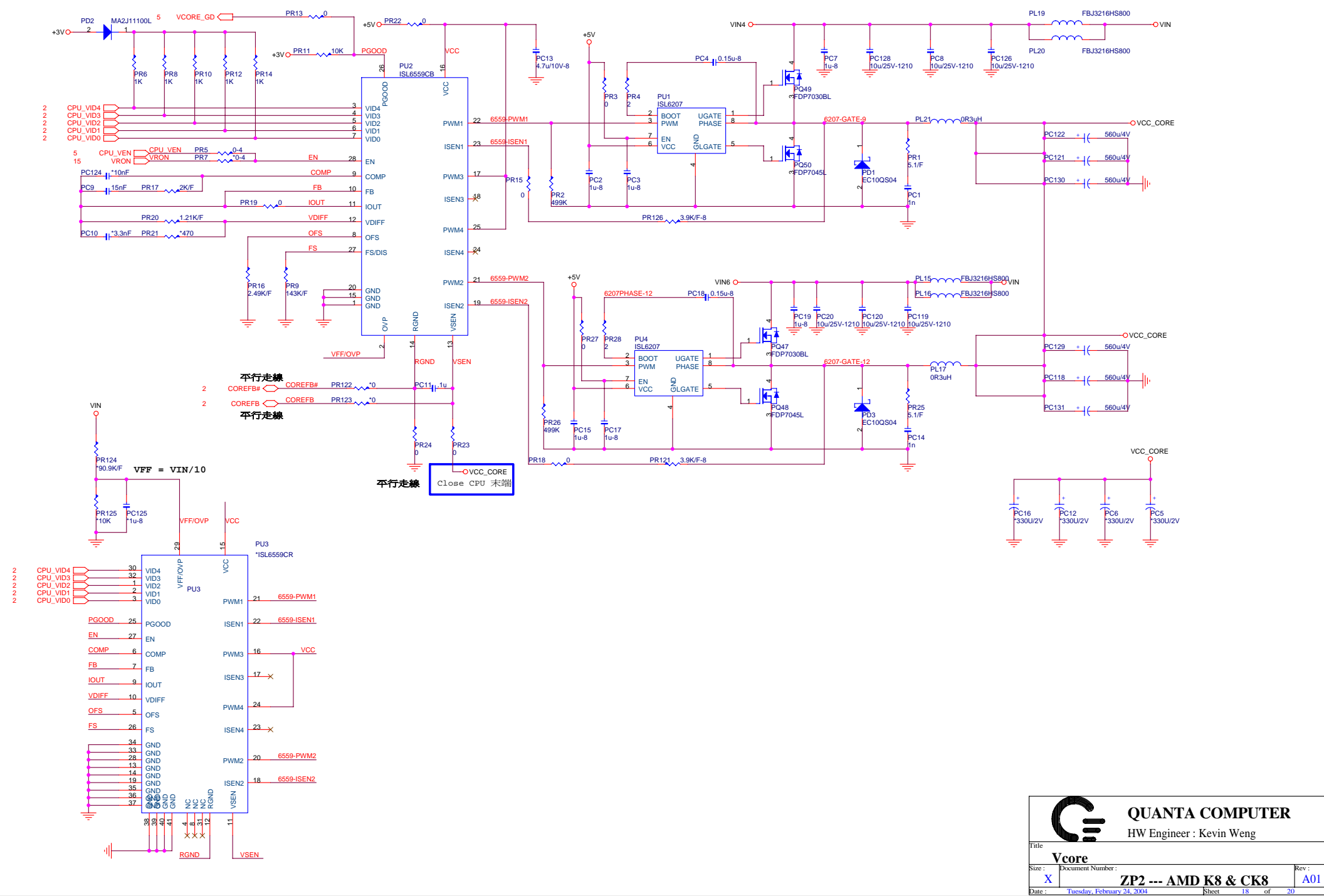
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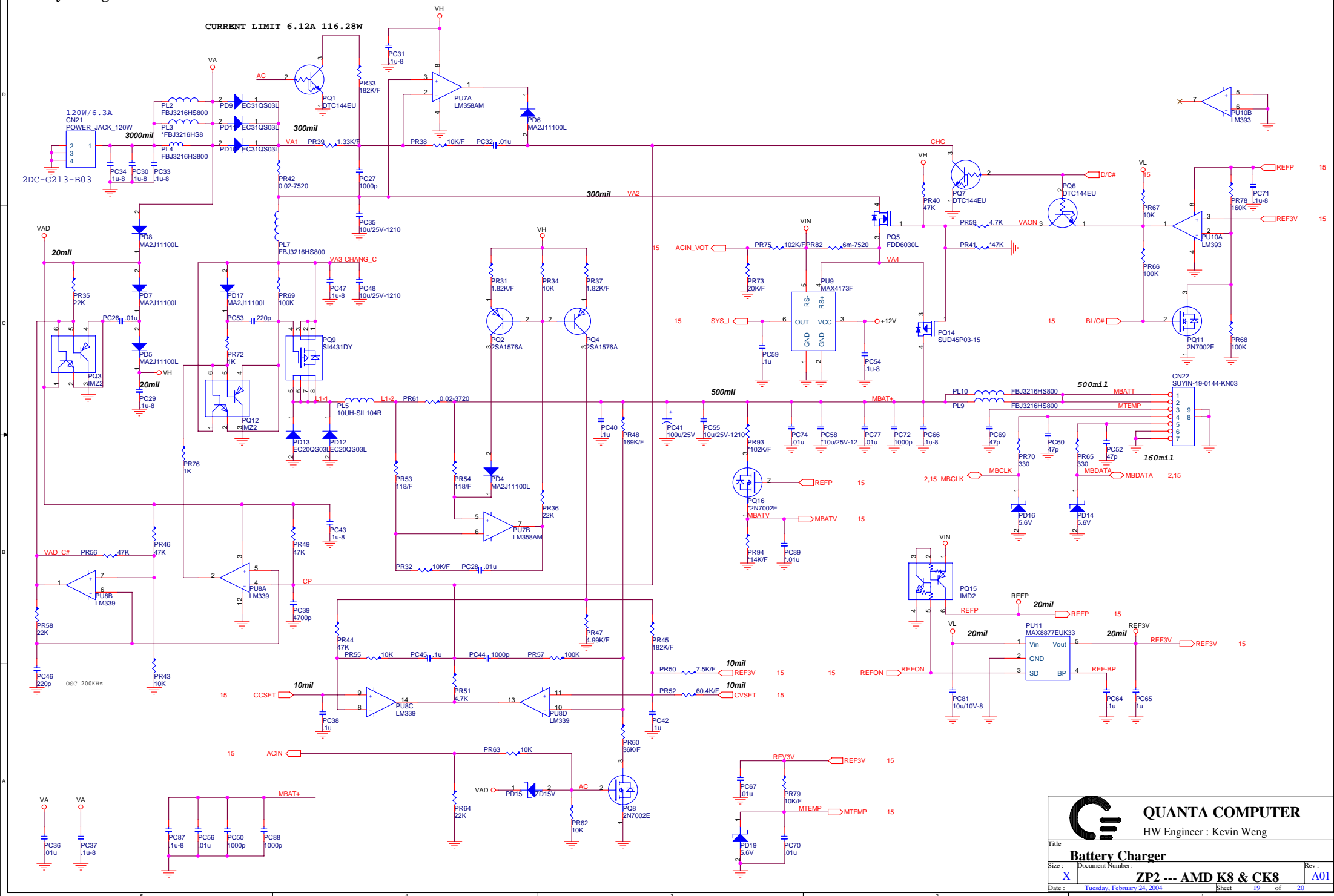
DDR Power



Vcore



## Battery Charger



**QUANTA COMPUTER**

HW Engineer : Kevin Weng

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## Battery Charger

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Discharge Circuit

